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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/065,909

11/28/2002

Tse-Hsien Yeh

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02/03/2006

NORTH AMERICA INTELLECTUAL PROPERTY CORPORATION

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EXAMINER

BURD, KEVIN MICHAEL

ART UNIT

PAPER NUMBER

2631

DATE MAILED: 02/03/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/065,909	Applicant(s) YEH, TSE-HSIEN	
	Examiner Kevin M. Burd	Art Unit 2631	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 November 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-17 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-17 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-17 are rejected under 35 U.S.C. 102(b) as being anticipated by Chao et al (US 6,047,026).

Regarding claim 1, Chao discloses a method of compensating for base line wander of a signal. The signal comprises a plurality of generated pulses. A signal is output from the FIR filter 14 in figure 9. The output of the filter is input to positive pulse slicer 18 and negative pulse slicer 16 for generating first (positive) and second (negative) pulses respectively (column 8, lines 24-27). An accumulated result is generated in sum 66 according to the number of first and second pulses. A baseline compensation signal is generated from the current sum signal and fed back to the filter 14. Additional information describing the circuit of figure 9 is found in column 7, line 30 to column 8, line 49.

Regarding claim 2, the current sum calculation will reset after correcting for the baseline wander.

Regarding claim 3, Chao further discloses the voltage level of the first pulse is greater than the voltage level of the second pulse since the voltage of the first pulse is positive and the voltage level of the second pulse is negative (figure 9). The current sum value will be decreased when the number of second pulses increase since the second pulse is subtracted from the first pulse.

Regarding claims 4-7, the received signal in the FIR filter will be corrected according to the correction signal that is dependent on the current sum value. This correction signal will increase or decrease the signal in the filter. For each pulse that is received, the feed back signal will increase or decrease a set amount (column 7, line 57 to column 8, line 11).

Regarding claims 8 and 15-17, Chao discloses a circuit for compensating for base line wander of a signal. The signal comprises a plurality of generated pulses. A signal is output from the FIR filter 14 in figure 9. The output of the filter is input to positive pulse slicer 18 and negative pulse slicer 16 for generating first (positive) and second (negative) pulses respectively (column 8, lines 24-27). An accumulated result is generated in sum 66 according to the number of first and second pulses. A baseline compensation signal is generated from the current sum signal and fed back to the filter 14. Additional information describing the circuit of figure 9 is found in column 7, line 30 to column 8, line 49.

Regarding claim 9, the current sum calculation will reset after correcting for the baseline wander.

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Regarding claim 10, the voltage level of the first pulse is greater than the voltage level of the second pulse since the voltage of the first pulse is positive and the voltage level of the second pulse is negative (figure 9). The current sum value will be decreased when the number of second pulses increase since the second pulse is subtracted from the first pulse.

Regarding claims 11-14, the received signal in the FIR filter will be corrected according to the correction signal that is dependent on the current sum value. This correction signal will increase or decrease the signal in the filter. For each pulse that is received, the feed back signal will increase or decrease a set amount (column 7, line 57 to column 8, line 11).

Conclusion


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin M. Burd whose telephone number is (571) 272-3008. The examiner can normally be reached on Monday - Friday 9 am - 5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on (571) 272-3021. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kevin M. Burd
2/1/2006


KEVIN BURD
PRIMARY EXAMINER